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Requirements and Compatibility | Ordering Information | Detailed Specifications | Pinouts/Front Panel Connections For user manuals and dimensional drawings, visit the product page resources tab on ni.com.

#### Last Revised: 2014-11-06 07:14:37.0

High-Speed M Series Multifunction DAQ for USB - 16-Bit, up to 1.25 MS/s, Integrated BNC Connectivity





- Up to 16 differential BNC analog inputs at 16 bits, 1.25 MS/s (1 MS/s scanning)
- Up to 4 BNC analog outputs at 16 bits, 2.86 MS/s
- /s NI-PGIA 2 and NI-MCal calibration technology for improved measurement accuracy
- Two 32-bit, 80 MHz counter/timers
- Up to 48 TTL/CMOS digital I/O lines (8 BNC, up to 32 hardware-timed at up to 1 MHz) NI signal streaming for 4 high-speed data streams on USB

- Analog and digital triggering supported; power supply included

NI-DAQmx driver software and LabVIEW SignalExpress LE included

#### Overview

With recent bandwidth improvements and new innovations from National Instruments, USB has evolved into a core bus of choice for measurement and automation applications. High-speed NI M Series devices for USB deliver high-performance data acquisition in an easy-to-use and portable form factor through USB ports on laptop computers and other portable computing platforms. NI created NI signal streaming, an innovative patent-pending technology that enables sustained bidirectional high-speed data streams on USB. The new technology, combined with advanced external synchronization and isolation, helps engineers and scientists achieve high-performance applications on USB.

NI M Series high-speed multifunction data acquisition (DAQ) modules for USB are optimized for superior accuracy at fast sampling rates. They provide an onboard NI-PGIA 2 amplifier designed for fast settling times at high scanning rates, ensuring 16-bit accuracy even when measuring all available channels at maximum speed. All high-speed devices have a minimum of 16 analog inputs, 24 digital I/O lines, seven programmable input ranges, analog and digital triggering, and two counter/timers. USB M Series devices are ideal for test, control, and design applications including portable data logging, field monitoring, embedded OEM, in-vehicle data acquisition, and academic. High-speed NI USB-625x M Series devices have an extended two-year calibration interval.

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Requirements and Compatibi	ility		
OS Information	Driver Information	Software Compatibility	
<ul> <li>Windows 2000/XP</li> </ul>	<ul> <li>NI-DAQmx</li> </ul>	<ul> <li>ANSI C/C++</li> </ul>	
Windows 7		<ul> <li>LabVIEW</li> </ul>	
<ul> <li>Windows Vista x64/x86</li> </ul>		<ul> <li>SignalExpress</li> </ul>	
		<ul> <li>Visual C#</li> </ul>	
		<ul> <li>Visual Studio .NET</li> </ul>	
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#### **Comparison Tables**

	Family	Connector	Analog Inputs	Resolution	Max Rate	Analog Outputs	Resolution	Max Rate	Digital I/O	Counter/ Timer
US	B-6251 BNC	BNC and screw	8 differential	16 bits	1.25 MS/s	2	16 bits	2.86 MS/s	24 (8 BNC, 8 clocked)	2
	USB-6259	BNC and screw	16 differential	16 bits	1.25 MS/s	4	16 bits	2.86 MS/s	48 (8 BNC, 32 clocked)	2

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## Application and Technology

## **NI Signal Streaming**

Unlike typical multifunction USB data acquisition devices, NI USB M Series DAQ devices incorporate NI signal streaming, a patent-pending technology that combines three innovative hardware- and software-level design elements to enable sustained high-speed and bidirectional data streams over USB. NI signal streaming, along with the error correction, noise rejection, power management, and power distribution inherent in the USB protocol, yields a robust, secure, and reliable bus. Without NI signal streaming, a multifunction data acquisition device could sustain only a single high-speed data stream, effectively making it a single-function device. For more information, visit **ni.com/usb**.

#### **USB M Series for Test**

For test, you can use the M Series high-speed analog inputs and 10 MHz digital lines with direct BNC connectivity for applications including test, component characterization, and sensor measurement. USB M Series multifunction DAQ devices also complement existing test systems that need additional measurement channels. For higher-channel-count signal conditioning on USB, consider the NI CompactDAQ or NI SCXI platform.

#### **USB M Series for Control**

USB M Series digital lines can drive 24 mA for relay and actuator control. By clocking the digital lines as fast as 10 MHz (with onboard regeneration), you can use these lines for pulse-width modulation (PWM) to control valves, motors, fans, lamps, and pumps. With four waveform analog outputs, two 80 MHz counter/timers, and four high-speed data streams on USB, M Series devices can execute multiple control loops simultaneously. High-speed USB-625x M Series devices also offer direct support for encoder measurements, protected digital lines, and digital debounce filters. With up to 32 single-ended analog inputs, 32 clocked digital lines, and four analog outputs, you can execute multiple control loops with a single device.

You can also create a complete custom motion controller by combining USB M Series devices with the NI SoftMotion Development Module.

#### USB M Series for Design

For design applications, you can use a wide range of I/O – from 32 single-ended analog inputs to 48 digital lines – to measure and verify prototype designs. USB M Series devices and NI LabVIEW SignalExpress interactive measurement software deliver benchtop measurements to the PC. With LabVIEW SignalExpress, you can quickly create design verification tests. The fast acquisition and generation rates of high-performance USB M Series high-speed devices along with LabVIEW SignalExpress provide fast design analysis. You can convert your tested and verified LabVIEW SignalExpress projects to LabVIEW applications for immediate M Series DAQ use, and bridge the gap between test, control, and design applications.

#### USB M Series for OEMs

Shorten your time to market by integrating National Instruments OEM products in your design. Board-only versions of USB M Series DAQ devices are available for OEM applications, with competitive quantity pricing and software customization. The NI OEM Elite Program offers free 30-day trial kits for qualified customers. Visit **ni.com/oem** for more information.

#### **Recommended Software**

National Instruments measurement services software, built around NI-DAQmx driver software, includes intuitive application programming interfaces, configuration tools, I/O assistants, and other tools designed to reduce system setup, configuration, and development time. National Instruments recommends using the latest version of NI-DAQmx driver software for application development in NI LabVIEW, LabVIEW SignalExpress, LabWindows™/CVI, and Measurement Studio. To obtain the latest version of NI-DAQmx, visit ni.com/support/daq/versions. NI measurement services software speeds up your development with features including:

- A guide to create fast and accurate measurements with no programming using the DAQ Assistant
- Automatic code generation to create your application in LabVIEW; LabWindows/CVI; LabVIEW SignalExpress; and Visual Studio .NET, ANSI C/C++, C#, or Visual Basic using Measurement Studio
- Multithreaded streaming technology for 1,000 times performance improvements
- Automatic timing, triggering, and synchronization routing to make advanced applications easy
- More than 3,000 free software downloads to jump-start your project available at ni.com/zone
- Software configuration of all digital I/O features without hardware switches/jumpers
- Single programming interface for analog input, analog output, digital I/O, and counters on hundreds of multifunction DAQ hardware devices

M Series devices are compatible with the following versions (or later) of NI application software – LabVIEW, LabWindows/CVI, or Measurement Studio versions 7.x or LabVIEW SignalExpress 2.x.

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#### **Ordering Information**

For a complete list of accessories, visit the product page on ni.com.

Products	Part Number	Recommended Accessories	Part Number
High-Speed M Series Multifunction DAQ for USE	3 with Integrated BNC Conne	ctivity	
USB-6251 BNC	780115-0P	No accessories required.	
USB-6259 BNC	780114-0P	No accessories required.	
Board-Only Devices for Embedded Systems and	IOEM		
USB-6251 OEM (Quantity 1)	194929-03	No accessories required.	
USB-6259 OEM (Quantity 1)	194929-01	No accessories required.	

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#### LabVIEW Professional Development System for Windows

Annual States		*******
	00- 50- X0-	70- 70- 64 (1712-100-100)
		*
Ó Ó	1001- 804- 804- 102- 100-	
	401- 222- 41- 40-	

# NI LabWindows™/CVI for Windows



- Advanced software tools for large project development
- Automatic code generation using DAQ Assistant and Instrument I/O Assistant
- Tight integration with a wide range of hardware
- Advanced measurement analysis and digital signal processing
- Open connectivity with DLLs, ActiveX, and .NET objects
- Capability to build DLLs, executables, and MSI installers
- Real-time advanced 2D graphs and charts
- Complete hardware compatibility with IVI, VISA, DAQ, GPIB, and serial
- Analysis tools for array manipulation, signal processing statistics, and curve fitting
- Simplified cross-platform communication with network variables
- Measurement Studio .NET tools (included in LabWindows/CVI Full only)
- The mark LabWindows is used under a license from Microsoft Corporation.

## SignalExpress for Windows



#### Quickly configure projects without programming

- Control over 400 PC-based and stand-alone instruments
- Log data from more than 250 data acquisition devices
- Perform basic signal processing, analysis, and file I/O
- Scale your application with automatic LabVIEW code generation
- Create custom reports or easily export data to LabVIEW, DIAdem or Microsoft Excel
- Customizable graphs and charts for WPF, Windows Forms, and ASP.NET Web Forms UI design
- Analysis libraries for array operations, signal generation, windowing, filters, signal processing
- Hardware integration support with native .NET data acquisition and instrument control libraries
- Automatic code generation for all NI-DAQmx data acquisition hardware
- Intelligent and efficient data-logging libraries for streaming measurement data to disk
- Support for Microsoft Visual Studio .NET 2012/2010/2008

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## Support and Services

## Calibration

NI measurement hardware is calibrated to ensure measurement accuracy and verify that the device meets its published specifications. To ensure the ongoing accuracy of your measurement hardware, NI offers basic or detailed recalibration service that provides ongoing ISO 9001 audit compliance and confidence in your measurements. To learn more about NI calibration services or to locate a qualified service center near you, contact your local sales office or visit ni.com/calibration.

## **Technical Support**

Get answers to your technical questions using the following National Instruments resources.

- Support Visit ni.com/support to access the NI KnowledgeBase, example programs, and tutorials or to contact our applications engineers who are located in NI sales offices around the world and speak the local language.
- Discussion Forums Visit forums.ni.com for a diverse set of discussion boards on topics you care about.
- Online Community Visit community.ni.com to find, contribute, or collaborate on customer-contributed technical content with users like you.

## Repair

While you may never need your hardware repaired, NI understands that unexpected events may lead to necessary repairs. NI offers repair services performed by highly trained technicians who quickly return your device with the guarantee that it will perform to factory specifications. For more information, visit ni.com/repair.

## **Training and Certifications**

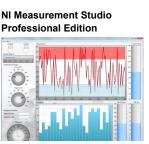
The NI training and certification program delivers the fastest, most certain route to increased proficiency and productivity using NI software and hardware. Training builds the skills to more efficiently develop robust, maintainable applications, while certification validates your knowledge and ability.

- Classroom training in cities worldwide the most comprehensive hands-on training taught by engineers.
- On-site training at your facility an excellent option to train multiple employees at the same time.
- Online instructor-led training lower-cost, remote training if classroom or on-site courses are not possible.
- Course kits lowest-cost, self-paced training that you can use as reference guides.
- Training memberships and training credits to buy now and schedule training later.

Visit ni.com/training for more information.

## Extended Warranty

NI offers options for extending the standard product warranty to meet the life-cycle requirements of your project. In addition, because NI understands that your requirements may change, the extended warranty is flexible in length and easily renewed. For more information, visit ni.com/warranty.



NI offers design-in consulting and product integration assistance if you need NI products for OEM applications. For information about special pricing and services for OEM customers, visit ni.com/oem.

## Alliance

Our Professional Services Team is comprised of NI applications engineers, NI Consulting Services, and a worldwide National Instruments Alliance Partner program of more than 700 independent consultants and integrators. Services range from start-up assistance to turnkey system integration. Visit ni.com/alliance.

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## **Detailed Specifications**

Specifications listed below are typical at 25 °C unless otherwise noted. Refer to the M Series User Manual for more information about NI 625x devices.

Analog Input	
Number of channels	
NI 6250/6251	8 differential or 16 single ended
NI 6254/6259	16 differential or 32 single ended
NI 6255	40 differential or 80 single ended
ADC resolution	16 bits
DNL	No missing codes guaranteed
INL	Refer to the AI Absolute Accuracy Table
Sampling rate	
Maximum	
NI 6250/6251/6254/6259	1.25 MS/s single channel, 1.00 MS/s multi-channel (aggregate)
NI 6255	1.25 MS/s single channel 750 kS/s multi-channel (aggregate)
Minimum	No minimum
Timing accuracy	50 ppm of sample rate
Timing resolution	50 ns
Input coupling	DC
Input range	±10 V, ±5 V, ±2 V, ±1 V, ±0.5 V, ±0.2 V, ±0.1 V
Maximum working voltage for analog inputs (signal + common mode)	±11 V of AI GND
Maximum working voltage for analog inputs (signal + common mode) CMRR (DC to 60 Hz)	±11 V of AI GND 100 dB
CMRR (DC to 60 Hz)	
CMRR (DC to 60 Hz) Input impedance	
CMRR (DC to 60 Hz) Input impedance Device on	100 dB
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND	100 dB >10 GΩ in parallel with 100 pF
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND	100 dB >10 GΩ in parallel with 100 pF
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Device off	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Device off Al+ to Al GND	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Device off Al+ to Al GND Al- to Al GND Al- to Al GND	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Al+ to Al GND Al+ to Al GND Al+ to Al GND Input bias current	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Al+ to Al GND Al+ to Al GND Al+ to Al GND Al+ to Al GND Crosstalk (at 100 kHz)	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω ±100 pA
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Al+ to Al GND Al+ to Al GND Al+ to Al GND Al+ to Al GND Input bias current Crosstalk (at 100 kHz) Adjacent channels	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω ±100 pA -75 dB
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Device off Al+ to Al GND Al- to Al GND Input bias current Crosstalk (at 100 kHz) Adjacent channels Non-adjacent channels	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω ±100 pA -75 dB -90 dB <sup>1</sup>
CMRR (DC to 60 Hz) Input impedance Device on Al+ to AI GND Al- to AI GND Al- to AI GND Al- to AI GND Input bias current Crosstalk (at 100 kHz) Adjacent channels Non-adjacent channels Small signal bandwidth (-3 dB)	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω 4100 pA -75 dB -90 dB <sup>1</sup> 1.7 MHz
CMRR (DC to 60 Hz) Input impedance Device on Al+ to Al GND Al- to Al GND Device off Al+ to Al GND Al- to Al GND Al- to Al GND Input bias current Crosstalk (at 100 kHz) Adjacent channels Non-adjacent channels Small signal bandwidth (-3 dB) Input FIFO size	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω 4100 pA -75 dB -90 dB <sup>1</sup> 1.7 MHz 4,095 samples
CMRR (DC to 60 Hz) Input impedance Device on Al+ to AI GND Al- to AI GND Al- to AI GND Al- to AI GND Input bias current Crosstalk (at 100 kHz) Adjacent channels Non-adjacent channels Small signal bandwidth (-3 dB) Input FIFO size Scan list memory	100 dB >10 GΩ in parallel with 100 pF >10 GΩ in parallel with 100 pF 820 Ω 820 Ω 4100 pA -75 dB -90 dB <sup>1</sup> 1.7 MHz 4,095 samples

Overvoltage protection (AI <079>, AI SENSE, AI SENSE 2)					
Device on	±25 V for up to four AI pins				
Device off	±15 V for up to four AI pins				
Input current during overvoltage condition	±20 mA max/Al pin				

<sup>1</sup> For USB-6255 devices, channel AI <0..15> crosstalk to channel AI <64..79> is -67 dB; applies to channels with 64-channel separation, for example, AI (x) and AI (x + 64).

#### Settling Time for Multichannel Measurements

NI 6250/6251/6254/6259

Range	±60 ppm of Step (±4 LSB for Full Scale Step)	±15 ppm of Step (±1 LSB for Full Scale Step)
±10 V, ±5 V, ±2 V, ±1 V	1 µs	1.5 μs
±0.5 V	1.5 µs	2 µs
±0.2 V, ±0.1 V	2 µs	8 µs

NI 6255

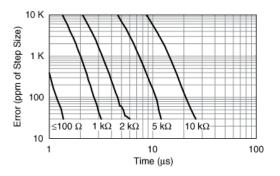
Range	±60 ppm of Step (±4 LSB for Full Scale Step)	±15 ppm of Step (±1 LSB for Full Scale Step)
±10 V, ±5 V, ±2 V, ±1 V	1.3 µs	1.6 µs
±0.5 V	1.8 µs	2.5 µs
±0.2 V, ±0.1 V	3 µs	8 µs

#### **Typical Performance Graphs**

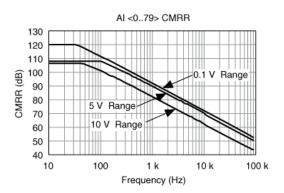
Settling Error Versus Time for Different Source Impedances 10 K Error (ppm of Step Size) 1 K 100 ≤1,00 Ω 1 kΩ 2 kΩ 5 kΟ 10 kΩ 10 1 10 100 Time (µs)

NI 6250/6251/6254/6259

NI 6255 Settling Error Versus Time for Different Source Impedances



AI <0..79> Small Signal Bandwidth Normalized Signal Amplitude (dB) 1 0 -1 -2 -3 -4 -5 -6 -7 -8∟ 1k 10 k 100 k 1000 k 10000 k Frequency (Hz)

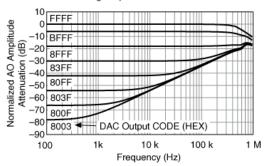


Analog Triggers	
Number of triggers	1
Source	
NI 6250/6251	AI <015>, APFI 0
NI 6254/6259	Al <031>, APFI <01>
NI 6255	AI <079>, APFI 0
Functions	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase
Source level	
Al <079>	±full scale
APFI <01>	±10 V
Resolution	10 bits, 1 in 1,024
Modes	Analog edge triggering, analog edge triggering with hysteresis, and analog window triggering
Bandwidth (-3 dB)	
Al <079>	3.4 MHz
APFI <01>	3.9 MHz
Accuracy	±1%
APFI <01> characteristics	
Input impedance	10 κΩ
Coupling	DC
Protection	
Power on	±30 V
Power off	±15 V
Analog Output	
Number of channels	
Number of channels NI 6250/6254	0
	0 2
NI 6250/6254	
NI 6250/6254 NI 6251/6255	2
NI 6250/6254 NI 6251/6255 NI 6259	2 4
NI 6250/6254           NI 6251/6255           NI 6259           DAC resolution	2 4 16 bits
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL	2 4 16 bits ±1 LSB
NI 6250/6254 NI 6251/6255 NI 6259 DAC resolution DNL Monotonicity	2 4 16 bits ±1 LSB 16 bit guaranteed
NI 6250/6254 NI 6251/6255 NI 6259 DAC resolution DNL Monotonicity Accuracy	2 4 16 bits ±1 LSB 16 bit guaranteed
NI 6250/6254 NI 6251/6255 NI 6259 DAC resolution DNL Monotonicity Accuracy Maximum update rate	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s 1.54 MS/s
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels         4 channels	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s 1.54 MS/s 1.54 MS/s
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels         4 channels         Timing accuracy	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s 1.54 MS/s 1.55 MS/s 50 ppm of sample rate
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels         4 channels         Timing accuracy         Timing resolution	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s 1.54 MS/s 1.25 MS/s 50 ppm of sample rate 50 ns
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels         4 channels         Timing accuracy         Timing resolution	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the <i>AO Absolute Accuracy Table</i> 2.86 MS/s 2.00 MS/s 1.54 MS/s 1.54 MS/s 50 ppm of sample rate 50 ns ±10 V, ±5 V, ±external reference on APFI <01>
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels         Timing accuracy         Timing resolution         Output coupling	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s 1.54 MS/s 1.54 MS/s 50 ppm of sample rate 50 ns ±10 V, ±5 V, ±external reference on APFI <01>
NI 6250/6254         NI 6251/6255         NI 6259         DAC resolution         DNL         Monotonicity         Accuracy         Maximum update rate         1 channel         2 channels         3 channels         4 channels         Timing accuracy         Utput range         Output range         Output impedance	2 4 16 bits ±1 LSB 16 bit guaranteed Refer to the AO Absolute Accuracy Table 2.86 MS/s 2.00 MS/s 1.54 MS/s 1.54 MS/s 1.25 MS/s 50 ppm of sample rate 50 ns ±10 V, ±5 V, ±external reference on APFI <01> DC

Power-on state	±5 mV <sup>2</sup>
Power-on glitch	1.5 V peak for 1.5 s
Output FIFO size	8,191 samples shared among channels used
Data transfers	
PCI/PCIe/PXI/PXIe devices	DMA (scatter-gather), interrupts, programmed I/O
USB devices	USB Signal Stream, programmed I/O
AO waveform modes:	
<ul> <li>Non-periodic waveform</li> <li>Periodic waveform regeneration mode from onboard FIFO</li> <li>Periodic waveform regeneration from host buffer including dynamic update</li> </ul>	
Settling time, full scale step 15 ppm (1 LSB)	2 µs
Slew rate	20 V/µs
Glitch energy at midscale transition, ±10 V range	
Magnitude	10 mV
Duration	1 µs
<sup>2</sup> For all USB-6251/6259 Screw Terminal devices, when powered on, the analog o	utput signal is not defined until after USB configuration is complete.
External Reference	
APFI <01> characteristics	

Coupling	DC
Protection	
Power on	±30 V
Power off	±15 V
Range	±11 V
Slew rate	20 V/µs





# Calibration (AI and AO)

Recommended warm-up time

Calibration interval

AI Absolute Accuracy Table

AI ADSOIU	ite Accurac	cy l'able								
Nomina Positive Full Scale	I Range Negative Full Scale	Residual Gain Error (ppm of Reading)	Gain Tempco (ppm/°C)	Reference Tempco	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	INL Error (ppm of Range)	Random Noise, σ (μVrms)	Absolute Accuracy at Full Scale <sup>1</sup> (μV)	Sensitivity <sup>2</sup> (μV)
10	-10	60	13	1	20	21	60	280	1,920	112.0
5	-5	70	13	1	20	21	60	140	1,010	56.0
2	-2	70	13	1	20	24	60	57	410	22.8
1	-1	80	13	1	20	27	60	32	220	12.8
0.5	- 0.5	90	13	1	40	34	60	21	130	8.4

15 minutes

2 years

0.2	- 0.2	130	13	1	80	55	60	16	74	6.4
0.1	- 0.1	150	13	1	150	90	60	15	52	6.0
Accuracies li	sted are valid	for up to two ye	ars from the d	evice externa	I calibration.					<i></i>
AbsoluteAcc	bsoluteAccuracy = Reading · (GainError) + Range · (OffsetError) + NoiseUncertainty									
GainError = I	GainError = ResidualAlGainError + GainTempco · (TempChangeFromLastInternalCal) + ReferenceTempco · (TempChangeFromLastExternalCal)									
OffsetError =	ResidualAIO	ffsetError + Offs	etTempco · (T	empChangel	FromLastInternal	Cal) + INL_Error				
NoiseUnce	NoiseUncertainty = $\frac{\text{RandomNoise} \cdot 3}{\sqrt{100}}$ For a coverage factor of 3 $\sigma$ and averaging 100 points.									
<sup>1</sup> Absolute ad	<sup>1</sup> Absolute accuracy at full scale on the analog input channels is determined using the following assumptions:									
TempChangeFromLastExternalCal = 10 °C										
TempChangeFromLastInternalCal = 1 °C										
number_of_readings = 100										
CoverageFactor = 3 σ										
For example, on the 10 V range, the absolute accuracy at full scale is as follows:										
GainError = 6	GainError = 60 ppm + 13 ppm · 1 + 1 ppm · 10 GainError = 83 ppm									
OffsetError =	OffsetError = 20 ppm + 21 ppm · 1 + 60 ppm OffsetError = 101 ppm									
NoiseUnce	NoiseUncertainty = $\frac{275 \ \mu V \cdot 3}{\sqrt{100}}$ NoiseUncertainty = 83 $\mu V$									
AbsoluteAcc	AbsoluteAccuracy = 10 V · (GainError) + 10 V · (OffsetError) + NoiseUncertainty AbsoluteAccuracy = 1920 µV									
<sup>2</sup> Sensitivity i	Sensitivity is the smallest voltage change that can be detected. It is a function of noise.									

AO Absolute Accuracy Table								
Nominal Positive Full Scale	Range Negative Full Scale	Residual Gain Error (ppm of Reading)	Gain Tempco (ppm/°C)	Reference Tempco	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	INL Error (ppm of Range)	Absolute Accuracy at Full Scale <sup>1</sup> (μV)
10	-10	75	17	1	40	2	64	2,080
5	-5	85	8	1	40	2	64	1,045

<sup>1</sup> Absolute Accuracy at full scale numbers is valid immediately following internal calibration and assumes the device is operating within 10 °C of the last external calibration.

Accuracies listed are valid for up to two years from the device external calibration.

AbsoluteAccuracy = OutputValue · (GainError) + Range · (OffsetError)

GainError = ResidualGainError + GainTempco · (TempChangeFromLastInternalCal) + ReferenceTempco · (TempChangeFromLastExternalCal)

OffsetError = ResidualOffsetError + AOOffsetTempco · (TempChangeFromLastInternalCal) + INL\_Error

# Digital I/O/PFI

Static Characteristics	
umber of channels	
NI 6250/6251/6255	24 total, 8 (P0.<07>), 16 (PFI <07>/P1, PFI <815>/P2)
NI 6254/6259	48 total, 32 (P0.<031>), 16 (PFI <07>/P1, PFI <815>/P2)
Ground reference	D GND
rection control	Each terminal individually programmable as input or output
I-down resistor	50 kΩ typ, 20 kΩ min
but voltage protection <sup>3</sup>	±20 V on up to two pins
Stresses beyond those listed under Input voltage protection may cause permanent	damage to the device.
Naveform Characteristics (Port 0 Only)	

Terminals used

NI 6250/6251/6255

NI 6254/6259

Port 0 (P0.<0..7>)

Port 0 (P0.<0..31>)

NI 6250/6251/6255       Up to 8 bits         NI 6254/6259       Up to 32 bits         Waveform generation (DO) FIFO       2,047 samples         Waveform acquisition (DI) FIFO       2,047 samples         DI Sample Clock frequency       2,047 samples         PCI/PCIe/PXI/PXIe devices       0 to 10 MHz <sup>4</sup> USB devices       0 to 10 MHz <sup>4</sup> DO Sample Clock frequency       0 to 10 MHz         PCI/PCIe/PXI/PXIe devices       0 to 10 MHz         Streaming from FIFO       0 to 10 MHz         VSB devices       0 to 10 MHz         VSB devices       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz         VSB devices       0 to 10 MHz         Streaming from memory       0 to 10 MHz	Port/sample size	
Waveform generation (DO) FIFO       2,047 samples         Waveform acquisition (DI) FIFO       2,047 samples         DI Sample Clock frequency       0 to 10 MHz <sup>4</sup> VBB devices       0 to 10 MHz <sup>4</sup> USB devices       0 to 1 MHz system dependent <sup>4</sup> DO Sample Clock frequency       PCI/PCIe/PXI/PXIe devices         PCI/PCIe/PXI/PXIe devices       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz         VSB devices       0 to 1 MHz system dependent <sup>4</sup> USB devices       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz         VSB devices       0 to 10 MHz system dependent <sup>4</sup> USB devices       0 to 10 MHz system dependent <sup>4</sup> Streaming from memory       0 to 10 MHz system dependent <sup>4</sup>	NI 6250/6251/6255	Up to 8 bits
Waveform acquisition (DI) FIFO       2,047 samples         DI Sample Clock frequency       0 to 10 MHz <sup>4</sup> PCI/PCIe/PXI/PXIe devices       0 to 10 MHz <sup>4</sup> DO Sample Clock frequency       0 to 10 MHz system dependent <sup>4</sup> DO Sample Clock frequency       PCI/PCIe/PXI/PXIe devices         PCI/PCIe/PXI/PXIe devices       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 10 MHz         VSB devices       0 to 10 MHz         Streaming from memory       0 to 10 MHz         Streaming from memory       0 to 10 MHz	NI 6254/6259	Up to 32 bits
DI Sample Clock frequency       0 to 10 MHz <sup>4</sup> VSB devices       0 to 1 MHz system dependent <sup>4</sup> DO Sample Clock frequency          PCI/PCIe/PXI/PXIe devices          Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 10 MHz         VSB devices          Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 10 MHz         Streaming from memory       0 to 10 MHz         Streaming from memory       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz	Waveform generation (DO) FIFO	2,047 samples
PCI/PCIe/PXI/PXIe devices       0 to 10 MHz <sup>4</sup> USB devices       0 to 1 MHz system dependent <sup>4</sup> DO Sample Clock frequency       PCI/PCIe/PXI/PXIe devices         PCI/PCIe/PXI/PXIe devices       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 1 MHz system dependent <sup>4</sup> USB devices       0 to 10 MHz         Streaming from memory       0 to 10 MHz         VSB devices       0 to 10 MHz system dependent <sup>4</sup> Streaming from memory       0 to 10 MHz	Waveform acquisition (DI) FIFO	2,047 samples
USB devices       0 to 1 MHz system dependent <sup>4</sup> DO Sample Clock frequency       PCI/PCle/PXI/PXIe devices         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 1 MHz system dependent <sup>4</sup> USB devices       USB devices         Regenerate from FIFO       0 to 1 0 MHz         Streaming from memory       0 to 1 0 MHz system dependent <sup>4</sup> USB devices       0 to 10 MHz         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 10 MHz	DI Sample Clock frequency	
DO Sample Clock frequency       PCI/PCle/PXI/PXIe devices       Regenerate from FIFO     0 to 10 MHz       Streaming from memory     0 to 1 MHz system dependent <sup>4</sup> USB devices       Regenerate from FIFO     0 to 10 MHz       Streaming from memory     0 to 10 MHz       USB devices     0 to 10 MHz       Regenerate from FIFO     0 to 10 MHz       Streaming from memory     0 to 10 MHz	PCI/PCIe/PXI/PXIe devices	0 to 10 MHz <sup>4</sup>
PCI/PCIe/PXI/PXIe devices         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 1 MHz system dependent <sup>4</sup> USB devices       Regenerate from FIFO         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 10 MHz	USB devices	0 to 1 MHz system dependent <sup>4</sup>
Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 1 MHz system dependent <sup>4</sup> USB devices       Regenerate from FIFO         Regenerate from FIFO       0 to 10 MHz         Streaming from memory       0 to 1 MHz system dependent <sup>4</sup>	DO Sample Clock frequency	
Streaming from memory       0 to 1 MHz system dependent <sup>4</sup> USB devices       Regenerate from FIFO         Streaming from memory       0 to 10 MHz         Streaming from memory       0 to 1 MHz system dependent <sup>4</sup>	PCI/PCIe/PXI/PXIe devices	
USB devices           Regenerate from FIFO         0 to 10 MHz           Streaming from memory         0 to 1 MHz system dependent <sup>4</sup>	Regenerate from FIFO	0 to 10 MHz
Regenerate from FIFO     0 to 10 MHz       Streaming from memory     0 to 1 MHz system dependent <sup>4</sup>	Streaming from memory	0 to 1 MHz system dependent <sup>4</sup>
Streaming from memory     0 to 1 MHz system dependent <sup>4</sup>	USB devices	
	Regenerate from FIFO	0 to 10 MHz
Data transfers	Streaming from memory	0 to 1 MHz system dependent <sup>4</sup>
	Data transfers	
PCI/PCIe/PXI/PXIe devices DMA (scatter-gather), interrupts, programmed I/O	PCI/PCIe/PXI/PXIe devices	DMA (scatter-gather), interrupts, programmed I/O
USB devices USB Signal Stream, programmed I/O	USB devices	USB Signal Stream, programmed I/O
DO or DI Sample Clock source <sup>5</sup> Any PFI, RTSI, AI Sample or Convert Clock, AO Sample Clock, Ctr <i>n</i> Internal O and many other signals	DO or DI Sample Clock source <sup>5</sup>	Any PFI, RTSI, AI Sample or Convert Clock, AO Sample Clock, Ctr <i>n</i> Internal Output, and many other signals

<sup>4</sup> Performance can be dependent on bus latency and volume of bus activity.

<sup>5</sup> The digital subsystem does not have its own dedicated internal timing engine. Therefore, a sample clock must be provided from another subsystem on the device or an external source.

### PFI/Port 1/Port 2 Functionality

Functionality Static digital input, static digital output, timing input, timing output

Timing output sources

Debounce filter settings

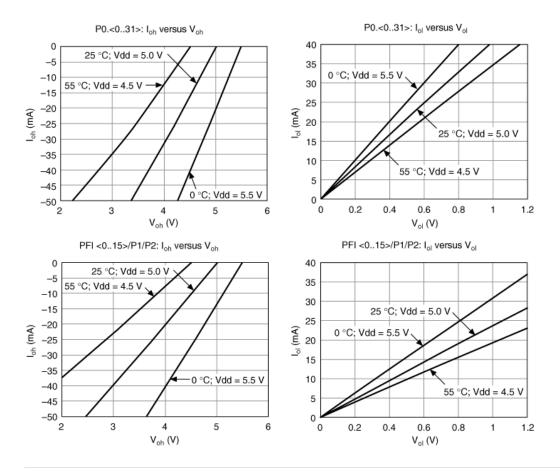
Many AI, AO, counter, DI, DO timing signals

125 ns, 6.425  $\mu s,$  2.56 ms, disable; high and low transitions; selectable per input

Recommended Operation Conditions <sup>6</sup>			
Level	Min	Max	
Input high voltage (V <sub>IH</sub> )	2.2 V	5.25 V	
Input low voltage (V <sub>IL</sub> )	0 V	0.8 V	
Output high current (I <sub>OH</sub> )	ĺ	ĺ	
P0.<031>	_	-24 mA	
PFI <015>/P1/P2	-	-16 mA	
Output low current (I <sub>OL</sub> )			
P0.<031>	_	24 mA	
PFI <015>/P1/P2	-	16 mA	

Electrical Characteristics			
Level	Min	Max	
Positive-going threshold (VT+) Negative-going threshold (VT-)	0.8 V	2.2 V	
Delta VT hysteresis (VT+ - VT-)	0.2 V	_	
$I_{IL}$ input low current (V <sub>in</sub> = 0 V) $I_{IH}$ input high current (V <sub>in</sub> = 5 V)	-	-10 μA 250 μA	

Digital I/O Characteristics<sup>6</sup>



<sup>6</sup> On earlier versions of the USB-6251 Screw Terminal (part numbers 194929A/B/C-0*x*) and the USB-6259 Screw Terminal (part numbers 194021B/C-0*x*), the digital I/O characteristics of P0.<16..31> match the characteristics of PFI <0..15>. Refer to the November 2006 version of the *NI 625x Specifications* (part number 371291G-01) for more details.

General-Fulpose Counter/Timers	
Number of counter/timers	2
Resolution	32 bits
Counter measurements	Edge counting, pulse, semi-period, period, two-edge separation
Position measurements	X1, X2, X4 quadrature encoding with Channel Z reloading; two-pulse encoding
Output applications	Pulse, pulse train with dynamic updates, frequency division, equivalent time sampling
Internal base clocks	80 MHz, 20 MHz, 0.1 MHz
External base clock frequency	0 MHz to 20 MHz
Base clock accuracy	50 ppm
Inputs	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Routing options for inputs	Any PFI, RTSI, PXI_TRIG, PXI_STAR, analog trigger, many internal signals
FIFO	2 samples
Data transfers	
PCI/PCIe/PXI/PXIe devices	Dedicated scatter-gather DMA controller for each counter/timer; interrupts, programmed I/O
USB devices	USB Signal Stream, programmed I/O
Frequency Generator	
Number of channels	1
Base clocks	10 MHz, 100 kHz
Divisors	1 to 16
Base clock accuracy	50 ppm

Output can be available on any PFI or RTSI terminal.

General-Purpose Counter/Timers

Phase-Locked Loop (PLL)	
Number of PLLs	1
Reference signal	PXI_STAR, PXI_CLK10, RTSI <07>
Output of PLL	80 MHz Timebase; other signals derived from 80 MHz Timebase including 20 MHz and 100 kHz Timebases
External Digital Triggers	
Source	Any PFI, RTSI, PXI_TRIG, PXI_STAR
Polarity	Software-selectable for most signals
Analog input function	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase
Analog output function	Start Trigger, Pause Trigger, Sample Clock, Sample Clock Timebase
Counter/timer functions	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Digital waveform generation (DO) function	Sample Clock
Digital waveform acquisition (DI) function	Sample Clock
Device-To-Device Trigger Bus	
PCI/PCIe devices	RTSI <07> <sup>7</sup>
PXI/PXIe devices	PXI_TRIG <07>, PXI_STAR
USB devices	None
Output selections	10 MHz Clock; frequency generator output; many internal signals
Debounce filter settings	125 ns, 6.425 $\mu s,$ 2.56 ms, disable; high and low transitions; selectable per input
<sup>7</sup> In other sections of this document, <i>RTSI</i> refers to RTSI <07> for PCI/PCIe of	devices or PXI_TRIG <07> for PXI/PXIe devices.
Bus Interface	
PCI/PXI devices	3.3 V or 5 V signal environment
PCIe devices	
Form factor	x1 PCI Express, specification v1.0a compliant
Slot compatibility	x1, x4, x8, and x16 PCI Express slots <sup>8</sup>
PXIe devices	
Form factor	x1 PXI Express peripheral module, specification rev 1.0 compliant

x1 and x4 PXI Express or PXI Express hybrid slots

counter/timer 1

counter/timer 0, counter/timer 1

6, analog input, analog output, digital input, digital output, counter/timer 0,

4, can be used for analog input, analog output, digital input, digital output,

Slot compatibility USB 2.0 Hi-Speed or full-speed<sup>9,10</sup>

USB devices DMA channels (PCI/PCIe/PXI/PXIe devices)

USB Signal Stream (USB devices)

All PXI-625x devices support one of the following features:

- May be installed in PXI Express hybrid slots

• Or, may be used to control SCXI in PXI/SCXI combo chassis

Table 1. PXI/SCXI Combo and PXI Express Chassis Compatibility				
M Series Device	M Series Part Number	SCXI Control in PXI/SCXI Combo Chassis	PXI Express Hybrid Slot Compatible	
PXI-6250	191325D-04/191325E-04L	No	Yes	
PXI-6251	191325D-03/191325E-03L	No	Yes	
F AI-0251	191325D-13/191325E-13L	Yes	No	
PXI-6254	191325D-02/191325E-03L	No	Yes	
PXI-6255	193618A-01	No	Yes	
PXI-6259	191325D-01/191325E-01L	No	Yes	
F AI-0209	191325D-11/191325E-11L	Yes	No	
Earlier versions of PXI-6251/ 6254/6259	191325C-0x 191325B-0x	Yes	No	

All NI PXIe-625x devices may be installed in PXI Express slots or PXI Express hybrid slots.

<sup>8</sup> Some motherboards reserve the x16 slot for graphics use. For PCI Express guidelines, refer to ni.com/pciexpress.

<sup>9</sup> If you are using a USB M Series device in full-speed mode, device performance will be lower and you will not be able to achieve maximum sampling/update rates.

<sup>0</sup> Operating on a full-speed bus may result in lower high-speed full-spe	rformance will be lower and you will not be able to achieve maximum sampling/update rates. eed performance.
Power Requirements	
Current draw from bus during no-load condition <sup>11</sup>	
PCI/PXI devices	
+5 V	0.03 A
+3.3 V	0.725 A
+12 V	0.35 A
PCIe devices	
+3.3 V	0.925 A
+12 V	0.35 A
PXIe devices	
+3.3 V	0.45 A
+12 V	0.5 A
Current draw from bus during AI and AO overvoltage condition <sup>11</sup>	
PCI/PXI devices	
+5 V	0.03 A
+3.3 V	1.2 A
+12 V	0.38 A
PCIe devices	
+3.3 V	1.4 A
+12 V	0.38 A
PXIe devices	
+3.3 V	0.48 A
+12 V	0.71 A
Caution USB-625 <i>x</i> devices must be powered with NI offered <i>A</i> for the device and has appropriate safety certification marks fo	C adapter or a National Electric Code (NEC) Class 2 DC source that meets the power requirements r country of use.
ISB power supply requirements	11 to 30 VDC, 20 W, locking or non-locking power jack with 0.080" diameter center pin, 5/16-32 thread for locking collars

<sup>11</sup> Does not include P0/PFI/P1/P2 and +5 V terminals.

**Power Limits** 

Caution Exceeding the power limits may cause unpredictable behavior by the dev	ice and/or PC/chassis.
PCI devices	
+5 V terminal (connector 0)	1 A max <sup>12</sup>
+5 V terminal (connector 1)	1 A max <sup>12</sup>
PCIe devices	
Without disk drive power connector installed	
+5 V terminals combined	0.35 A max <sup>12</sup>
P0/PFI/P1/P2 and +5 V terminals combined	0.39 A max
With disk drive power connector installed	
+5 V terminal (connector 0)	1 A max <sup>12</sup>
+5 V terminal (connector 1)	1 A max <sup>12</sup>
P0/PFI/P1/P2 combined	0.39 A max
PXI/PXIe devices	
+5 V terminal (connector 0)	1 A max <sup>12</sup>

+5 V terminal (connector 1)	1 A max <sup>12</sup>
P0/PFI/P1/P2 and +5 V terminals combined	2 A max
USB devices	
+5 V terminal	1 A max <sup>12</sup>
P0/PFI/P1/P2 and +5 V terminals combined	2 A max
Power supply fuse	2 A, 250 V
<sup>12</sup> Has a self-resetting fuse that opens when current exceeds this specification.	
Physical Requirements	
r nysical requirements	
Printed circuit board dimensions	
NI PCI-6250/6251/6254/6255/6259	9.7 × 15.5 cm (3.8 × 6.1 in.)
NI PCIe-6251/6259	9.9 × 16.8 cm (3.9 × 6.6 in.) (half-length)
NI PXI/PXIe-6250/6251/6254/6255/6259	Standard 3U PXI
Enclosure dimensions (includes connectors)	
NI USB-6251/6255/6259 Screw Terminal	26.67 × 17.09 × 4.45 cm (10.5 × 6.73 × 1.75 in.)
NI USB-6251/6259 BNC	28.6 × 17 × 6.9 cm (11.25 × 6.7 × 2.7 in.)
NI USB-6251/6255/6259 Mass Termination	18.8 × 17.09 × 4.45 cm (7.4 × 6.73 × 1.75 in.)
NI USB-6251/6255/6259 OEM	Refer to the NI USB-622x/625x OEM User Guide
Weight	
NI PCI-6250	142 g (5 oz)
NI PCI-6251	149 g (5.2 oz)
NI PCI-6254	152 g (5.3 oz)
NI PCI-6255	164 g (5.8 oz)
NI PCI-6259	162 g (5.6 oz)
NI PCIe-6251	161 g (5.7 oz)
NI PCIe-6259	175 g (6.1 oz)
NI PXI-6250	212 g (7.5 oz)
NI PXI-6251/6254	222 g (7.8 oz)
NI PXI-6255	236 g (8.3 oz)
NI PXI-6259	233 g (8.2 oz)
NI PXIe-6251	208 g (7.3 oz)
NI PXIe-6259	221 g (7.8 oz)
NI USB-6251 Screw Terminal	1.2 kg (2 lb 10 oz)
NI USB-6255/6259 Screw Terminal	1.24 kg (2 lb 11 oz)
NI USB-6251/6255/6259 Mass Termination	816 g (1 lb 12.8 oz)
NI USB-6251 OEM	140 g (4.9 oz)
NI USB-6255/6259 OEM	172 g (6.1 oz)
I/O connector	
NI PCI/PCIe/PXI/PXIe-6250/6251	1 68-pin VHDCI
NI PCI/PCIe/PXI/PXIe-6254/6255/6259	2 68-pin VHDCI
NI USB-6251 Screw Terminal	64 screw terminals
NI USB-6255/6259 Screw Terminal	128 screw terminals
NI USB-6251 BNC	21 BNCs and 30 screw terminals
NI USB-6259 BNC	32 BNCs and 60 screw terminals
	52 BINGS and 60 screw terminals
NI USB-6251 Mass Termination	1 68-pin SCSI

Disk drive power connector (PCIe devices)	Standard ATX peripheral connector (not serial ATA)					
USB-6251/6255/6259 Screw Terminal/USB-6251/6259 BNC screw terminal wiring	16-28 AWG					
Maximum Working Voltage <sup>13</sup>						
NI 6250/6251/6254/6255/6259 channel-to-earth	11 V, Measurement Category I					
Caution Do not use for measurements within Categories II, III, or IV.						
<sup>13</sup> Maximum working voltage refers to the signal voltage plus the common-mode voltage						
Environmental						
Operating temperature						
PCI/PXI/PXIe devices	0 to 55 °C					
PCIe devices	0 to 50 °C					
USB devices	0 to 45 °C					
Storage temperature	-20 to 70 °C					
Humidity	10 to 90% RH, noncondensing					
Maximum altitude	2,000 m					
Pollution Degree (indoor use only)	2					
Shock and Vibration (PXI/PXIe Devices Only)						
Operational shock	30 g peak, half-sine, 11 ms pulse (Tested in accordance with IEC-60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.)					
Random vibration						
Operating	5 to 500 Hz, 0.3 g <sub>rms</sub>					
Nonoperating	5 to 500 Hz, 2.4 g <sub>rms</sub> (Tested in accordance with IEC-60068-2-64. Nonoperating test profile exceeds the requirements of MIL-PRF-28800F, Class 3.)					

### Safety

This product is designed to meet the requirements of the following standards of safety for electrical equipment for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1

• UL 61010-1, CSA 61010-1

Note For UL and other safety certifications, refer to the product label or visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

#### **Electromagnetic Compatibility**

This product is designed to meet the requirements of the following standards of EMC for electrical equipment for measurement, control, and laboratory use:

- EN 61326 EMC requirements; Minimum Immunity
- EN 55011 Emissions; Group 1, Class A
- CE, C-Tick, ICES, and FCC Part 15 Emissions; Class A
- Note For EMC compliance, operate this device with shielded cables.

## **CE Compliance**

This product meets the essential requirements of applicable European Directives, as amended for CE marking, as follows:

- 73/23/EEC; Low-Voltage Directive (safety)
- 89/336/EEC; Electromagnetic Compatibility Directive (EMC)
- Note Refer to the Declaration of Conformity (DoC) for this product for any additional regulatory compliance information. To obtain the DoC for this product, visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

## **Environmental Management**

National Instruments is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial not only to the environment but also to NI customers.

For additional environmental information, refer to the NI and the Environment Web page at ni.com/environment. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

### Waste Electrical and Electronic Equipment (WEEE)

At the end of their life cycle, all products must be sent to a WEEE recycling center. For more information about WEEE recycling centers and National Instruments WEEE initiatives, visit ni.com/environment/weee.htm.

## 电子信息产品污染控制管理办法 (中国 RoHS)

中国客户 National Instruments 符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。
 关于 National Instruments 中国 RoHS 合規性信息, 请登录 ni.com/environment/rohs\_china。
 (For information about China RoHS compliance, go to ni.com/environment/rohs\_china,)

# **Pinouts/Front Panel Connections**

AI 9 AI GND AI 2 AI 10 AI GND AI 3 1 AI 11 1 AI GND 1 AI SENSE 1 AI GND 1 AO 0 1	1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6	000000000000000000000000000000000000000	17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32	AI 4 AI 2 AI 3 AI 3 AI 3 AI 3 AI 3 AI 3 AI 3 AI 3	P0.0 P0.1 P0.2 P0.3 P0.4 P0.5 P0.6 P0.7 PFI 0/P1.0 PFI 1/P1.1 PFI 2/P1.2 PFI 3/P1.3 PFI 4/P1.4 PFI 5/P1.5 PFI 6/P1.6 PFI 7/P1.7	65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80	000000000000000000000000000000000000000	<u>୭୭୭୭୭୭୭୭୭୭୭୭</u> ୭୭୭୭୭୭୭୭	81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96	PFI 8/P2.0 D GND PFI 9/P2.1 D GND PFI 10/P2.2 D GND PFI 11/P2.3 D GND PFI 12/P2.4 D GND PFI 13/P2.5 D GND PFI 14/P2.6 D GND PFI 14/P2.6 D GND PFI 15/P2.7 +5 V
NI USB-6251 Screw Terminal Pinout										

Al 0 1 Al 1 2 Al 2 3 Al 3 Al 4 Al 4 5 Al 5 Al 5 Al 5 Al 6 Al 5 Al 6 Al 6 Al 6 Al 6 Al 6 Al 6 Al 6 Al 6	AI 19       33       49       AI 27         AI 20       34       50       AI 28         AI 21       35       51       AI 28         AI 21       35       50       53       AI GND         AI 23       38       54       AI 31         AI 33       40       56       AI 41         AI 34       41       56       AI 42         AI 35       42       39       56       AI 41         AI 34       41       56       AI 43       59       AI 42         AI 36       43       59       AI 44       60       62       AI 45         AI 38       46       56       62       AI 46       63       AI 47         AI 39       47       56       AI 47       64       AI 56
AI 49       65       S       81       AI 57         AI 50       66       S       82       AI 58         AI 51       67       S       83       AI 59         AI 52       68       S       84       AI 60         AI 53       69       S       86       AI 62         AI 55       71       S       87       AI 63         AI 55       71       S       89       AI 72         AI 64       73       S       89       AI 72         AI 65       74       S       90       AI 73         AI 65       74       S       91       AI 74         AI 65       76       S       92       AI 75         AI 66       75       S       92       AI 75         AI 68       77       S       93       AI 76         AI 69       78       S       95       AI 78         AI 70       79       S       96       AI 79	P0.0         97         S         F         %         113         PFI 8/P2.0           P0.1         98         S         114         D GND         115         PFI 9/P2.1           P0.3         100         S         115         PFI 9/P2.1         116         D GND           P0.4         101         S         S         117         PFI 10/P2.2         118         D GND           P0.5         102         S         117         PFI 11/P2.3         S         120         D GND           P0.6         103         S         120         D GND         122         D GND           PFI 0/P1.0         105         S         122         D GND         123         PFI 13/P2.5           PFI 4/P1.4         109         S         124         D GND         125         PFI 14/P2.6           PFI 6/P1.5         110         S         125         PFI 14/P2.6         126         D GND           PFI 6/P1.6         111         S         127         PFI 15/P2.7         128         +5 V

NI USB-6255 Screw Terminal Pinout

AI 0         1         S         17         AI 4           AI 8         2         S         18         AI 12           AI GND         3         S         20         AI 5           AI 1         4         S         20         AI 5           AI 9         5         S         21         AI 13           AI GND         6         S         22         AI GND           AI 2         7         S         22         AI GND           AI 2         7         S         24         AI 14           AI GND         9         S         25         AI GND           AI GND         9         S         22         AI GND           AI GND         9         S         25         AI GND           AI GND         10         S         28         AI GND           AI GND         12         S         30         AI GND           AI GND         12         S         30         AI GND           AI GND         14         S         30         AI GND           AI GND         14         S         30         AI GND           AO 0         15         <	AI 16       33       S       49       AI 20         AI 24       34       S       50       AI 28         AI GND       35       S       51       AI GND         AI 17       36       S       52       AI 21         AI 25       37       S       S       53       AI 29         AI GND       38       S       55       AI 22         AI 18       39       S       56       AI 30         AI GND       41       S       S68       AI 30         AI GND       41       S       S68       AI 23         AI 27       43       S       S       59       AI 31         AI GND       44       S       S       S6       AI 23         AI GND       44       S       S       S6       AI 23         AI 27       43       S       S       S6       AI 23         AI SENSE 2       45       S       S       S6       AI 23         AO 2       47       S       S       S       S64       AO 3         AO GND       48       S       S       S       S       S64       AO GND
P0.0         65         S         81         PFI 8/P2.0           P0.1         66         S         82         D GND           P0.2         67         S         83         PFI 9/P2.1           P0.3         68         S         85         PFI 10/P2           P0.4         69         S         86         D GND           P0.7         70         S         86         D GND           P0.6         71         S         89         PFI 11/P2           P0.7         72         S         89         D GND           PFI 0/P1.0         73         S         S         90         D GND           PFI 2/P12         75         S         S         92         D GND           PFI 3/P1.3         76         S         S         92         D GND           PFI 4/P1.4         77         S         S         94         D GND           PFI 6/P1.5         78         S         S         94         D GND           PFI 6/P1.6         79         S         S         96         +5 V           PFI 7/P1.7         80         S         S         S         S	P0.8         97         S         114         D GND           P0.10         99         S         S         114         D GND           2         P0.10         99         S         S         115         P0.25           2         P0.11         100         S         S         117         P0.26           3         P0.13         102         S         S         118         D GND           4         P0.15         104         S         S         120         D GND           5         P0.17         106         S         S         122         D GND           5         P0.18         107         S         S         123         P0.29           6         P0.99         108         S         124         D GND           6         P0.20         109         S         125         P0.30           P0.21         109         S         125         P0.30

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